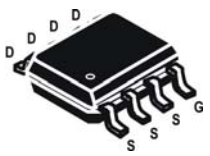
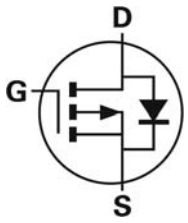
SGP3003S

-30V P-CHANNEL Power MOSFET

	SOP-8	
V_{DSS} , -30V $R_{DS(ON)}$, 4.7mΩ (max.) @ V_{GS}=-10V $R_{DS(ON)}$, 7.5mΩ (max.) @ V_{GS}=-4.5V I_D , -18A		

Description

The SGP3003S uses advanced trench technology MOSFETs to provide excellent $R_{DS(ON)}$ and low gate charge.

The complementary Power MOSFETs may be used in H-bridge, Inverters and other applications.

Features

- Low On-Resistance
- Low Input Capacitance
- Low Miller Charge
- Low Input/Output Leakage

Applications

- Motor / Body Load Control
- Automotive Systems
- Load Switch

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SGP3003S	Halogen-Free	SOP-8	S	Tape & Reel	3,000

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Drain Current-Continuous	T_A=25°C	I_D	-18	A
	T_A=75°C		-14	A
Drain Current-Pulsed Note 1		I_{DM}	-92	A
Maximum Power Dissipation	T_A=25°C	P_D	2.3	W
	T_A=75°C		1.4	W
Storage Temperature Range		T_{STG}	-55 to +150	°C
Operating Junction Temperature Range		T_J	-55 to +150	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient Note 2	$R_{\theta JA}$	Steady State	-	54	-	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	Steady State	-	19	-	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_{DS}=-250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-30V, V_{GS}=0V	-	-	-1	μA
Gate-Body Leakage	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Gate Threshold Voltage	$V_{GS(TH)}$	V_{DS}=V_{GS}, I_{DS}=-250μA	-1.2	-	-2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=-10V, I_{DS}=-18A	-	-	4.7	mΩ
		V_{GS}=-4.5V, I_{DS}=-10A	-	-	7.5	mΩ
Gate Resistance	R_g	V_{GS}=0V, V_{DS}=0V, f=1MHz	-	2.3	-	Ω

DYNAMIC CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Input Capacitance	C_{iss}	V_{DS}=-15V, V_{GS}=0V, f=1MHz	-	8404	-	pF
Output Capacitance	C_{oss}		-	913	-	
Reverse Transfer Capacitance	C_{rss}		-	434	-	
Forward Transconductance	gfs	V_D=-10V, I_D=-5A	-	22	-	S

SWITCHING CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Turn-On Delay Time	$T_{d(on)}$	V_{DD}=-15V, V_{GS}=-10V, R_G=6Ω, I_D=-1A	-	14.0	-	ns
Rise Time	t_r		-	19.6	-	
Turn-Off Delay Time	$T_{d(off)}$		-	191	-	
Fall Time	t_f		-	70.1	-	
Total Gate Charge at ____V	Q_g	V_{DS}=-15V, V_{GS}=-10V, I_D=-10A	-	141	-	nC
Gate to Source Gate Charge	Q_{gs}		-	28.5	-	
Gate to Drain “Miller” Charge	Q_{gd}		-	18.6	-	

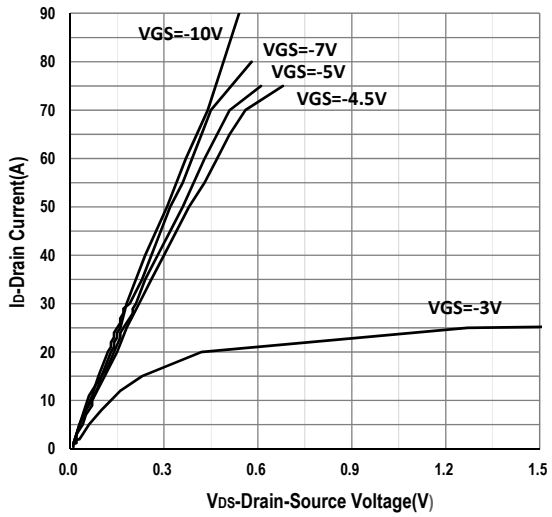
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Drain-Source Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_S=-1A	-	-	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	V_{DD}=-15V, I_F=-10A, di/dt=100A/μs	-	39.5	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	39.4	-	nC

Notes:

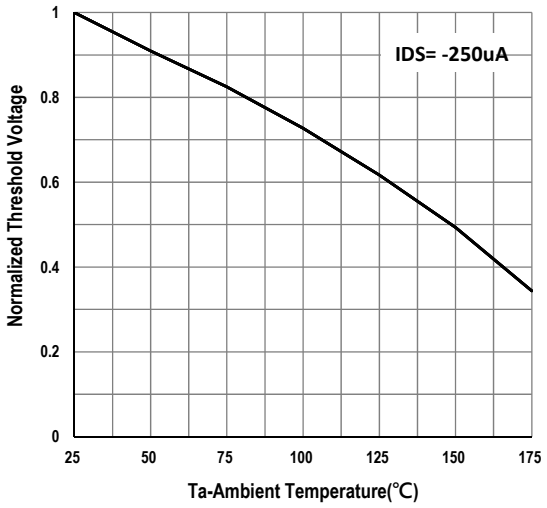
1. Pulse Test: Pulse Width ≤ 100ms, Duty Cycle ≤ 1%.
2. For surface-mounted devices, both $R_{\theta CA}$ and $R_{\theta JC}$ are measured with the device mounted on approximately 1”×1” FR-4 PCBs. In actual applications, many factors including the PCB material and layout, may affect the thermal resistance of the device-board assembly. For best results, characterize the thermal resistance directly in the application circuit.

Typical Operating Characteristics

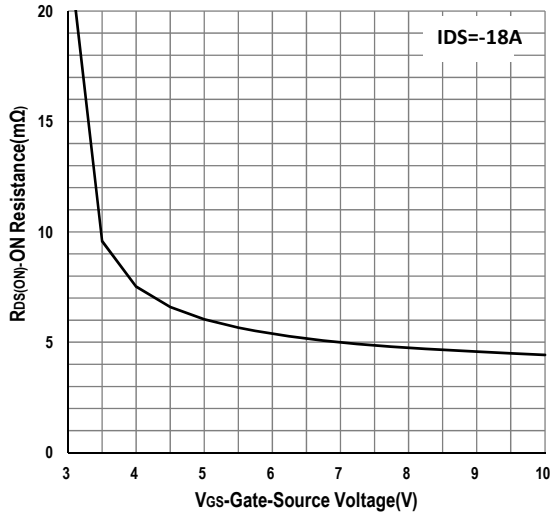
Output Characteristics



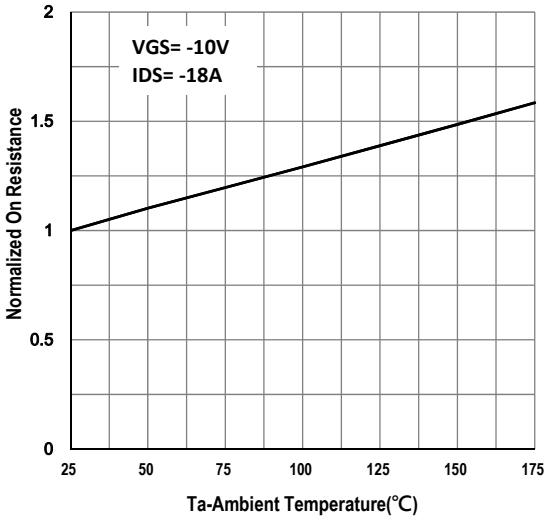
Gate Threshold Voltage



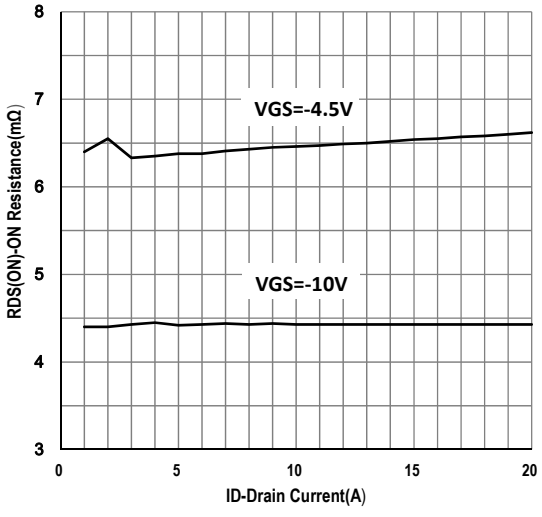
Gate-Source On Resistance



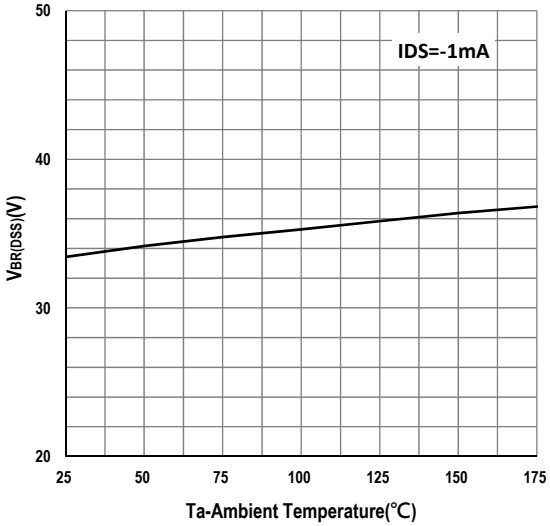
Drain-Source On Resistance



Drain-Source On Resistance

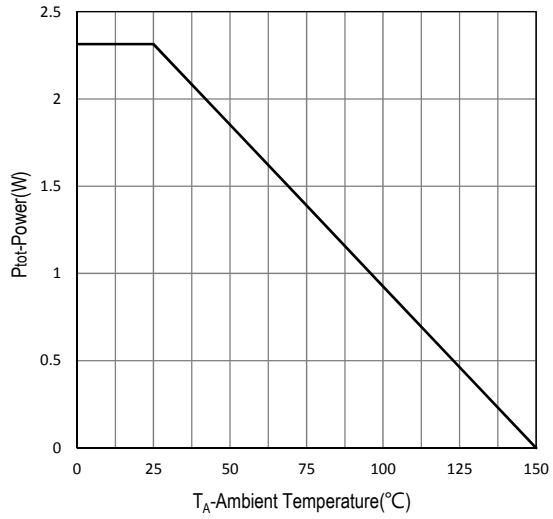


Drain-source Breakdown Voltage

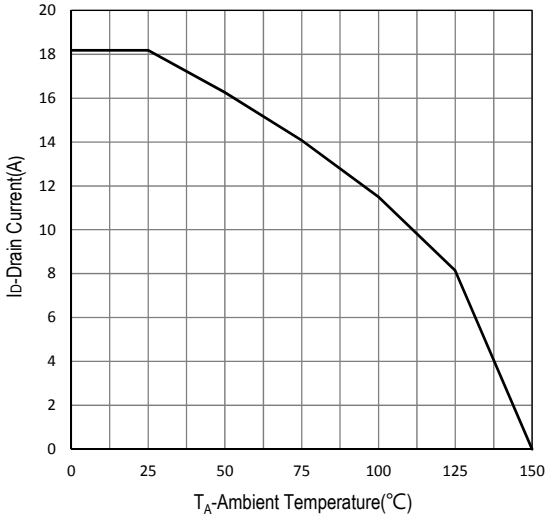


Typical Operating Characteristics (Cont.)

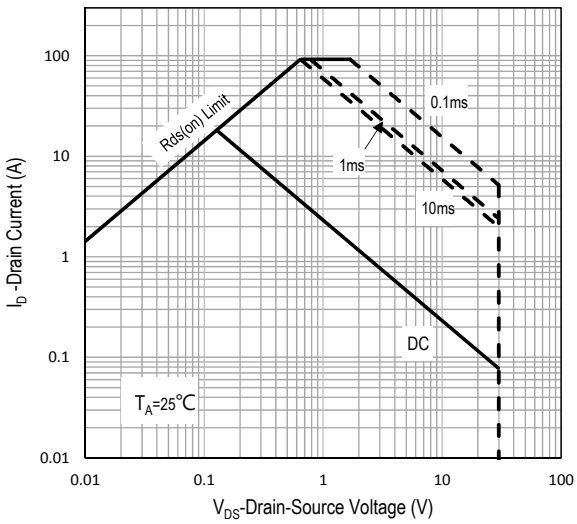
Power Dissipation



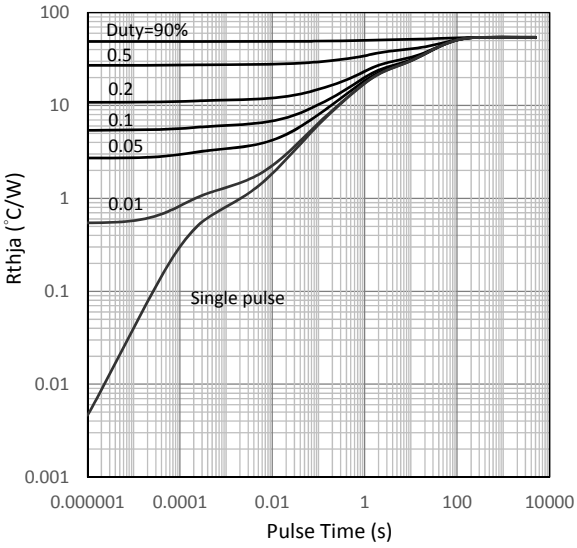
Drain Current



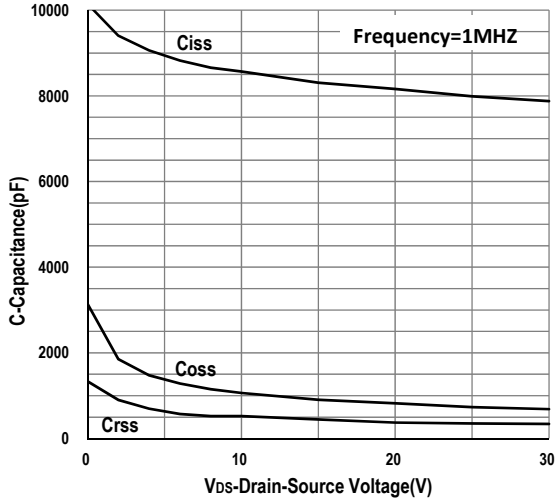
Safe Operation Area



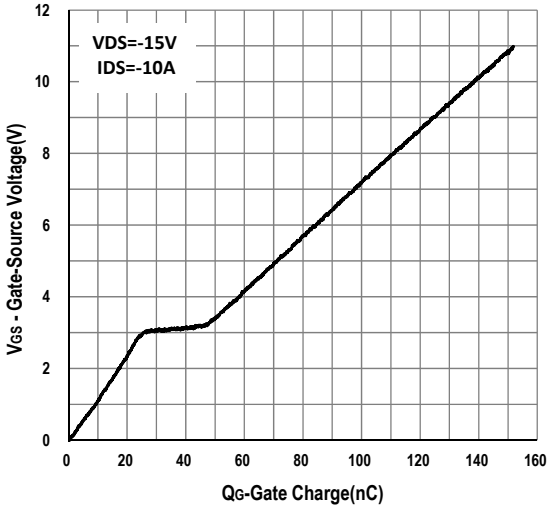
Transient Thermal Impedance



Capacitance



Gate Charge

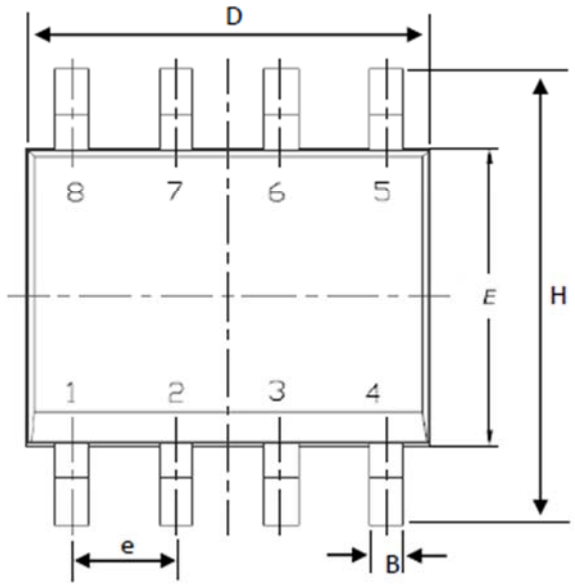


Marking Information

SOP-8 (S)	Marking Rule
Laser Marking  	Line 1：Device Name SGP3003S Line 2：Date Code YYMMXXX YY：Year Code MM：Month Code XXX：Serial Number

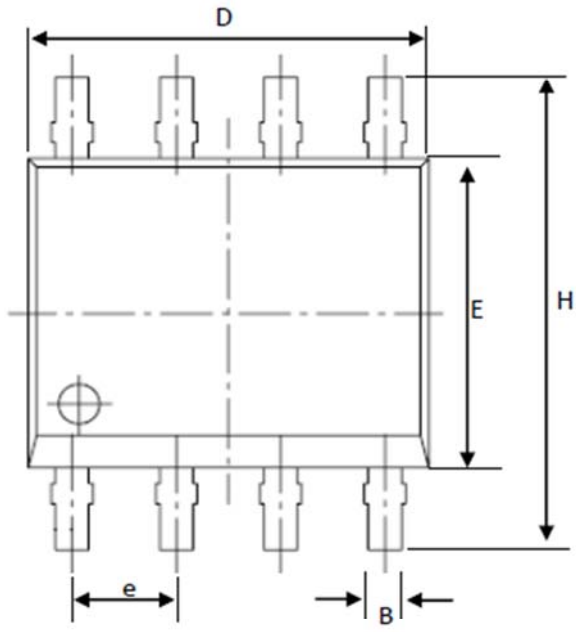
Package of Dimension

G-TYPE



Symbol	Min	Nor	Max
A	1.35	1.55	1.75
A1	0.10	0.18	0.25
B	0.31	0.41	0.51
c	0.17	0.21	0.25
D	4.80	4.90	5.00
E	3.80	3.90	4.00
e	1.27	1.27	1.27
H	5.80	6.00	6.20
L	0.40	0.84	1.27
α	0.00	4.00	8.00

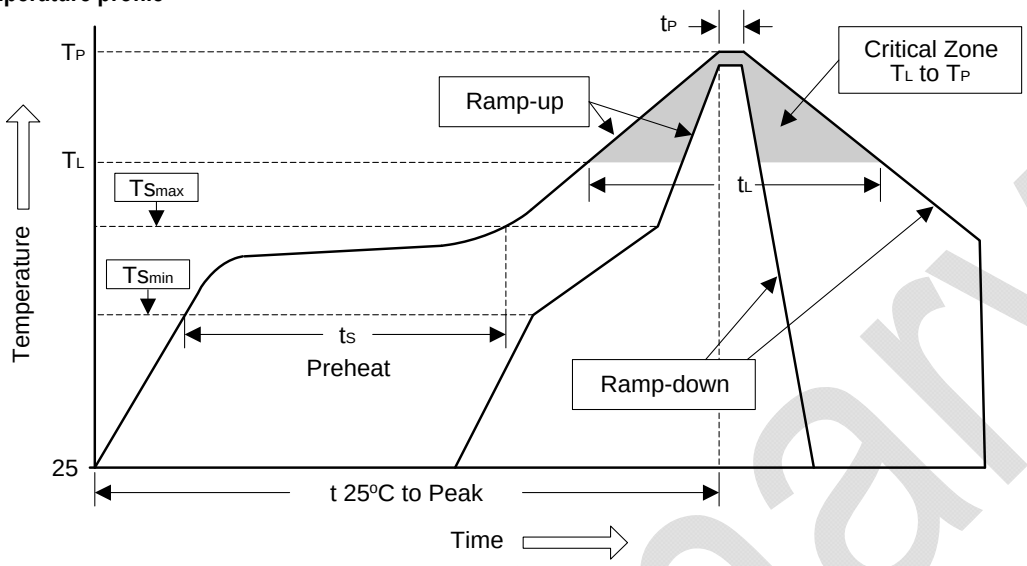
B-TYPE



Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat - Temperature Min (Ts_{min}) - Temperature Max (Ts_{max}) - Time (min to max) (ts)	 100°C 150°C 60 to 120 sec	 150°C 200°C 60 to 180 sec
Tsmax to T_L - Ramp-up Rate	 <3°C/sec	 <3°C/sec
Time maintained above: - Temperature (T_L) - Time (t_L)	 183°C 60 to 150 sec	 217°C 60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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